

Docket No. YOR920030540US1
YOR.499



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Motika, et al.

Serial No.: 10/780,878

Group Art Unit: Unknown

Filed: February 19, 2004

Examiner: Unknown

For: METHOD AND STRUCTURE FOR PICOSECOND-IMAGING-CIRCUIT-
ANALYSIS BASED BUILT-IN-SELF-TEST DIAGNOSTIC

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF REPLACEMENT FORMAL DRAWINGS

Sir:

Submitted herewith are seventeen (17) sheets of replacement formal drawings comprising Figures 1 - 21 for the above-identified patent application. Please substitute these replacement formal drawings for the formal drawings that were filed with the application.

Approval and acknowledgment of receipt of the drawings are respectfully requested.

Respectfully submitted,

Frederick E. Cooperrider, Esq.
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Date: 5/6/04

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